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For Immediate Release

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CYPRESS EXTENDS ALDEC SUPPORT TO NEXT-GENERATION *WARP*™ SOFTWARE

Added Functionality To Include HDL Graphical Design Entry and Full Behavioral Simulation

SAN JOSE, California...April 26, 2000 -- Cypress Semiconductor Corporation (NYSE:CY) today announced that it will include more Aldec Inc. tools in future high-end *Warp*™ products. The agreement extends an arrangement under which Cypress bundles Aldec timing simulation capabilities in its \$99 *Warp* software product.

Under the new agreement, Cypress will continue to include Aldec's Graphical Finite State Machine Editor and Dynamic Timing Simulator in its \$99 *Warp* software. Both of these tools are based on Aldec's popular Active-HDL™ product. In future *Warp* products, Cypress also will provide additional, high-end Aldec tools, such as a graphical HDL block editor, automatic text HDL to graphical design translation, HDL source code debugging, and full behavioral and timing simulation with testbench support.

"The extension of our alliance with Aldec provides benefits to all *Warp* users," said Christopher Norris, vice president of Cypress's Programmable Logic Division. "Users of today's product continue to get access to an extremely affordable HDL design system capable of supporting large, complex designs. And users of future *Warp* products will receive an innovative, high-end solution, providing them with even greater design flexibility and ease-of-use."

"Cypress has made its mark in the industry as a provider of quality HDL-based solutions," said Stanley Hyduke, president of Aldec. "The agreement to extend our software support to future generations of *Warp* products is a direct extension of Aldec's 'HDL for the masses' approach."

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Cypress is the only vendor to write its own HDL synthesis, and therefore *Warp* software delivers performance results that are unrivaled in the industry. Cypress sells *Warp* software over the Internet and through toll-free phone lines in the U.S. *Warp* software supports all of Cypress's programmable logic devices including the Ultra37000™ family of In-System Reprogrammable (ISR™) CPLDs.

Aldec, Inc., headquartered in Henderson, Nevada, produces a universal suite of Windows-based EDA tools that allows design engineers to implement their designs using several different design entry methods (schematic capture, state machine, VHDL, Verilog, or ABEL). Active-HDL, Active-VHDL™ and Active-CAD™ incorporate patented simulation technology and several design entry tools that provide a complete design entry and simulation solution. Founded in 1984, the company continues to evolve in the Windows-based EDA market as the fastest growing, privately held EDA supplier in the world. Additional information about Aldec is available at <http://www.aldec.com>.

About Cypress

Cypress Semiconductor provides high-performance integrated circuit solutions "By Engineers. For Engineers.™" for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static RAMs; high-bandwidth multiport and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

More than two-thirds of Cypress's sales come from fast-growing datacom/telecom markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

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Cypress employs more than 3,900 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at <http://www.cypress.com/investor/index.html>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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